

Radar Clipper for 23cms

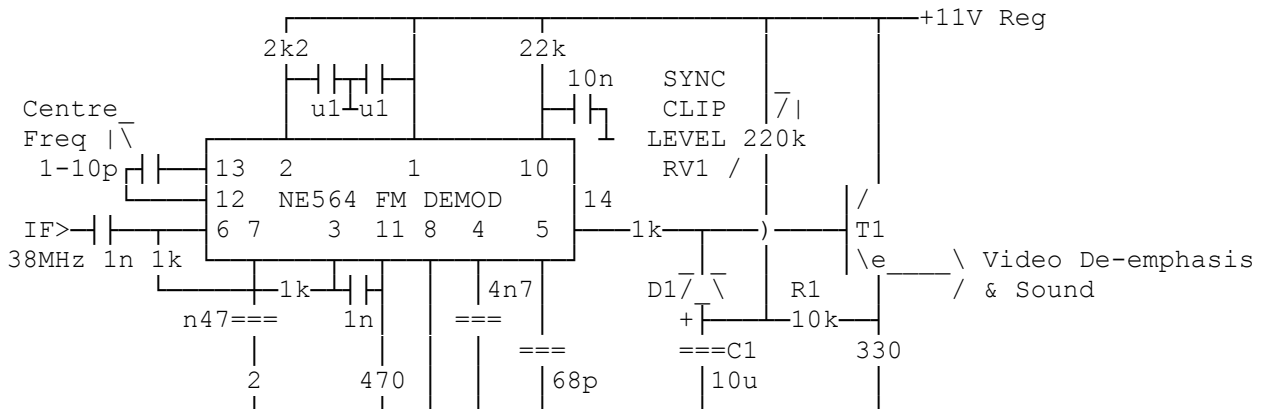
By G8MNY

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(8 Bit ASCII Graphics use code page 437 or 850)

This circuit was developed to limit the large negative pulses from radar QRM on 23cms ATV that are recovered video from a NE564 demodulator (or similar). These pulses had been effecting the video amp DC levels, resulting in short periods of no sync. The use of negative clipper after the de-emphasis network was not used as that would allow the radar pulses to damage the video syncs, by integrating them with the syncs before the clipper.

Using a complementary circuit (Inverted e.g. a T1=PNP, diode reversed, & the 330Ω & 220k to the other power rails) will give a peak white clipper circuit.



Clipping in the buffer before de-emphasis was more effective. The effect of loosing the sound subcarrier during radar clipping was un-noticeable.

The circuit takes the average buffered DC video level with R1 & C1. RV1 is used to just get D1 to clip the high impedance base signal, when a white picture is present by adjusting VR1.

Why don't U send an interesting bul?

73 de John G8MNY @ GB7CIP